# GOVT. POLYTECHNIC, HAMIRPUR (H.P.)
## Practical Planning

**Branch:** Computer Engineering

**Subject:** Major Project
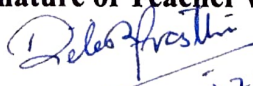
**Teacher:** Amit Nayyer and Ritesh Awasthi

**Semester: 6**th

**Session:** Jan 2024

**Laboratory:** Yes

| Sr. No. | No of Practical hours planned | Aim of the Practical | Remarks |
|---|---|---|---|
| 1 | 4 | Problem analysis | |
| 2 | 4 | Feasibility Study | |
| 3 | 6 | Use case diagrams | |
| 4 | 18 | Development of modules | |
| 5 | 8 | Testing of each module for verification and validation. | |
| 6 | 2 | Combination of various modules to construct a system. | |
| 7 | 2 | Testing of system. | |
| 8 | 2 | Execution of project. | |
| 9 | 10 | Report writing and submission. | |

**Signature of Teacher with Date**

**Signature of HOD**